NOIDA INSTITUTE OF ENGINEERING & TECHNOLOGY, GREATER NOIDA

SEMESTER (ODD)

II

**UNIT: -**

**Subject Name: -**

Logic Design and Computer Architecture

**Subject Code: -**

ACSAI0302

3 November 2022

**Submission Date: -**

1. What is divide overflow?
2. Explain Mantissa and Exponent with help of an example.
3. Design the 4 bit Carry Look Ahead Adder and explain its operation.
4. What is an Array Multiplier? Explain 2 bit by 2 bit array multiplier.
5. Explain the Addition and Subtraction of floating-point numbers with help of flowchart.
6. Show the systematic multiplication process of (20) X (-19) using Booths Algorithm.
7. Explain IEEE standard for floating point representation.
8. Explain Arithmetic and Logical unit (ALU) using proper diagram.
9. Explain Restoring method of Division Algorithm with help of flowchart.
10. Represent the number (85.125)10 in single precision and double precision format.